

**IN THE CLAIMS:**

Please amend the claims to read as the following:

Claims 1-4 (Canceled).

Claim 5 (Currently Amended): A method for supplying a power to a liquid crystal display, comprising the steps of:

taking a power source voltage less than 3.0V from a system.

supplying the power source voltage to digital circuit devices including an interface circuit, a timing controller, a data driving circuit, and a gate driving circuit for processing digital signal, wherein the interface circuit includes a low voltage differential signaling receiver which lowers a voltage level of the signals input from the system to thereby reduce the number of signal lines needed to the system and the timing controller.

Claim 6 (Previously Presented): The method for supplying a power to a liquid crystal display according to claim 5, further comprising the steps of:

raising or reducing the power source voltage from the system to generate voltage to be supplied to the liquid crystal panel.

Claims 7-17 (Canceled).

Claim 18 (Currently Amended): An apparatus for supplying a power to a liquid crystal display comprising:

a system for generating a power voltage under 3.0V; and

digital circuit devices including an interface circuit, a timing controller, a data driving circuit, and a gate driving circuit used to process the digital signal by taking the power voltage, wherein the interface circuit includes a low voltage differential signaling receiver which lowers a voltage level of the signals input from the system to thereby reduce the number of signal lines needed to the system and the timing controller.

Claim 19 (Previously Presented): The apparatus for supplying a power to a liquid crystal display according to claim 18, further comprising a DC-DC converter for raising or reducing the power source voltage to generate the raise or the reduced voltage to be supplied to the liquid crystal panel.

Claim 20 (Previously Presented): The apparatus for supplying a power of a liquid crystal display according to claim 18, wherein the digital circuit devices include:

an interface circuit for receiving a synchronous signal, a clock signal and digital video data from the system;

a data driving circuit for supplying the digital video data to the liquid crystal panel;

a gate driving circuit for supplying a scan pulse to the liquid crystal panel; and

a timing controller for controlling the data driving circuit and the gate driving circuit by using the synchronous signal and the clock signal from the interface circuit.

Claim 21 (Canceled).